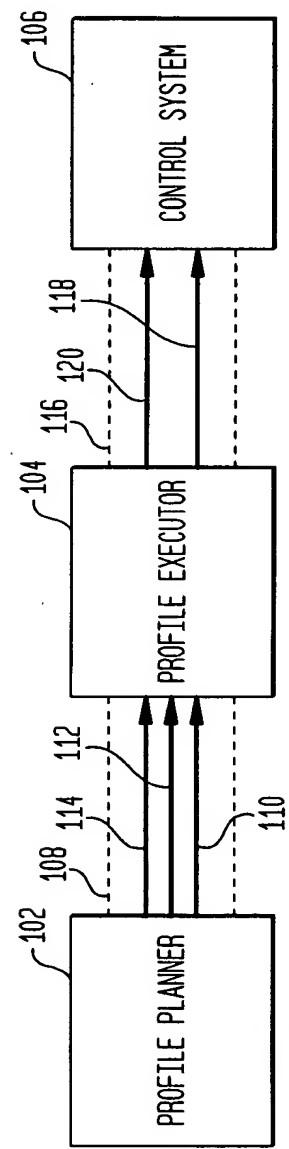


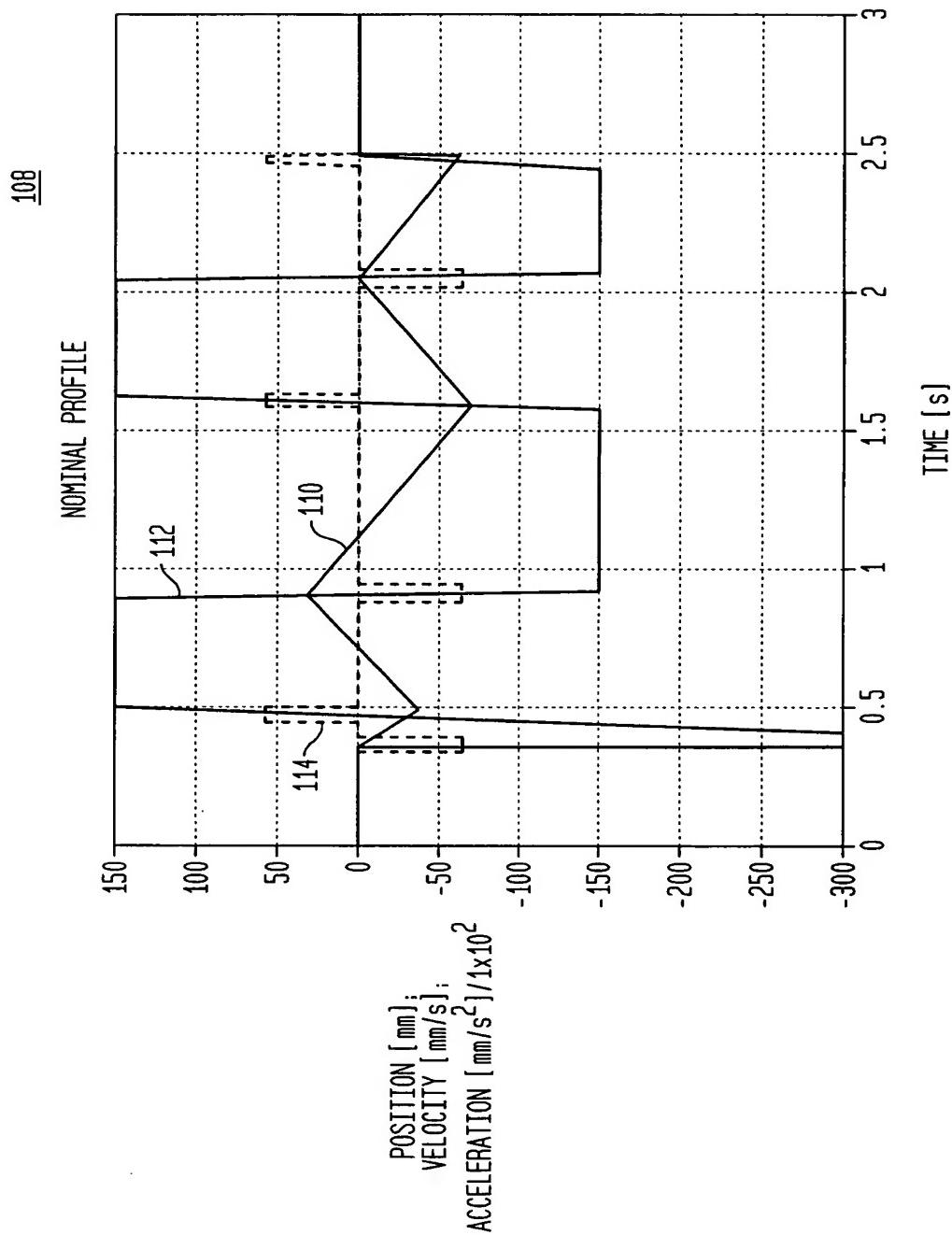
Appl. No. *To Be Assigned*; Filed: *Herewith*
Dkt. No. 1857.0330001; Group Art Unit: *To Be Assigned*
Inventor: Roberto B. Wiener; Tel.: (202) 371-2600
Title: Method and System for Efficient and Accurate
Processing of a Discrete Time Input Signal

FIG. 1



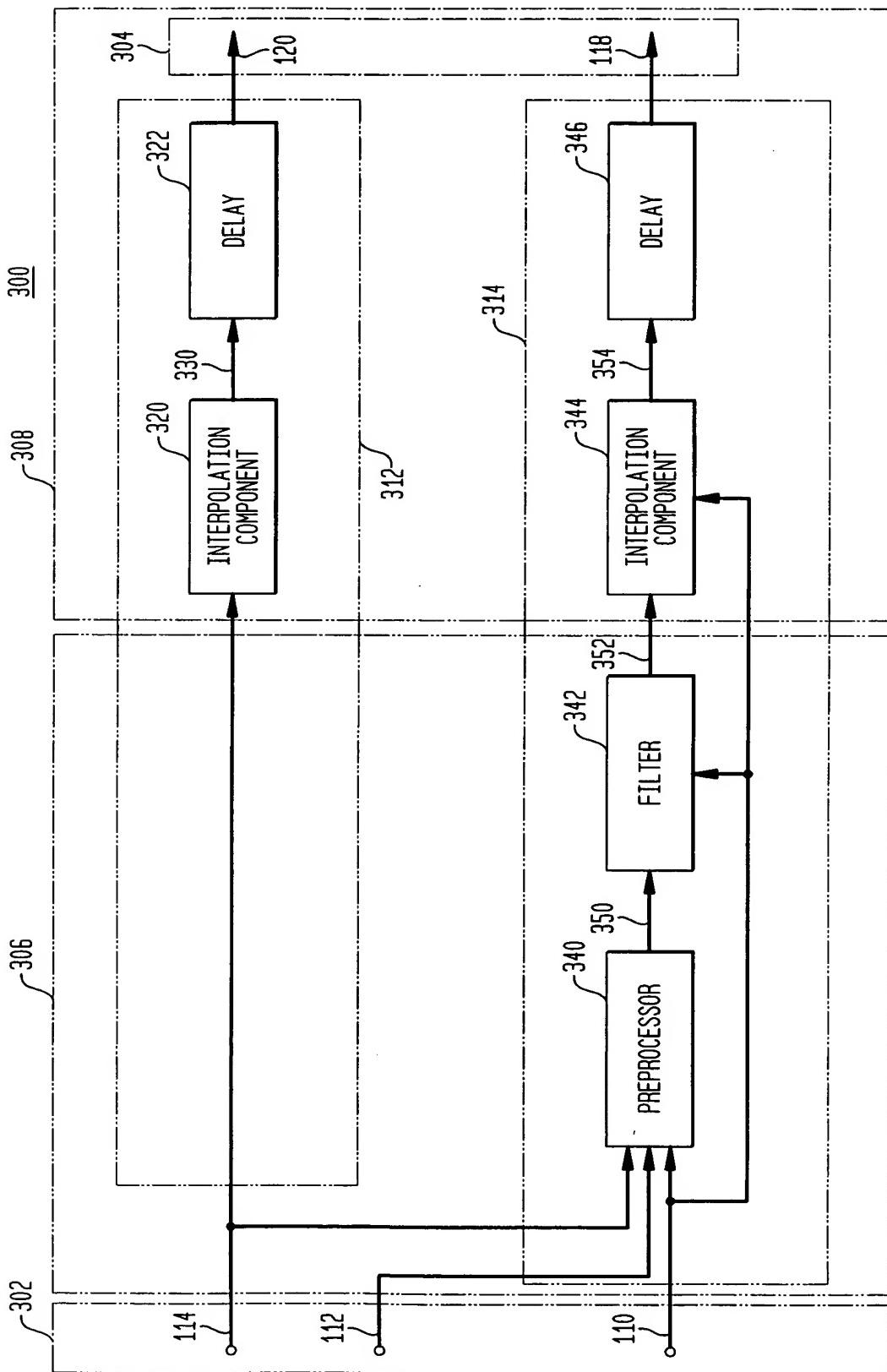
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FIG. 2



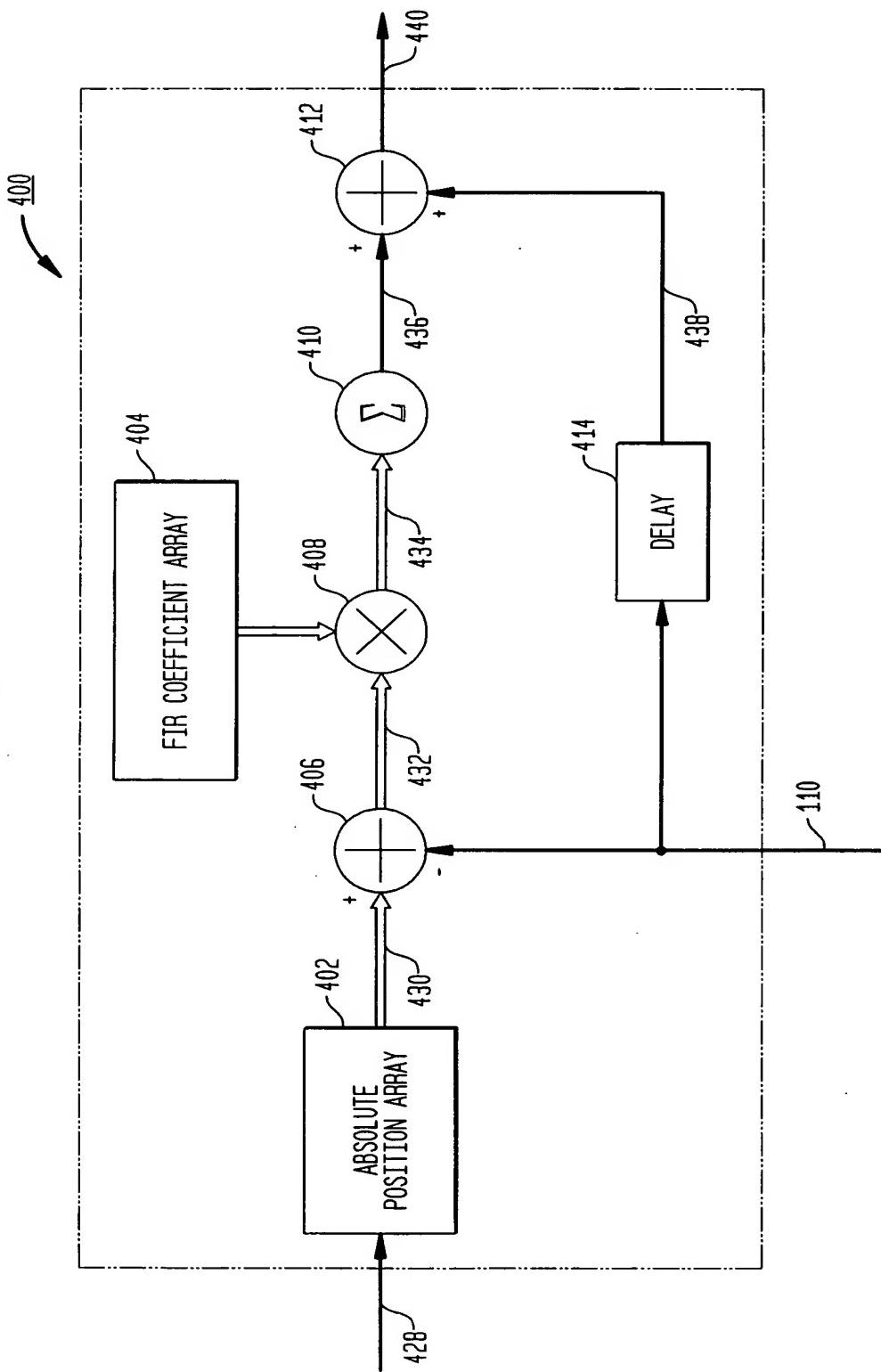
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FIG. 3



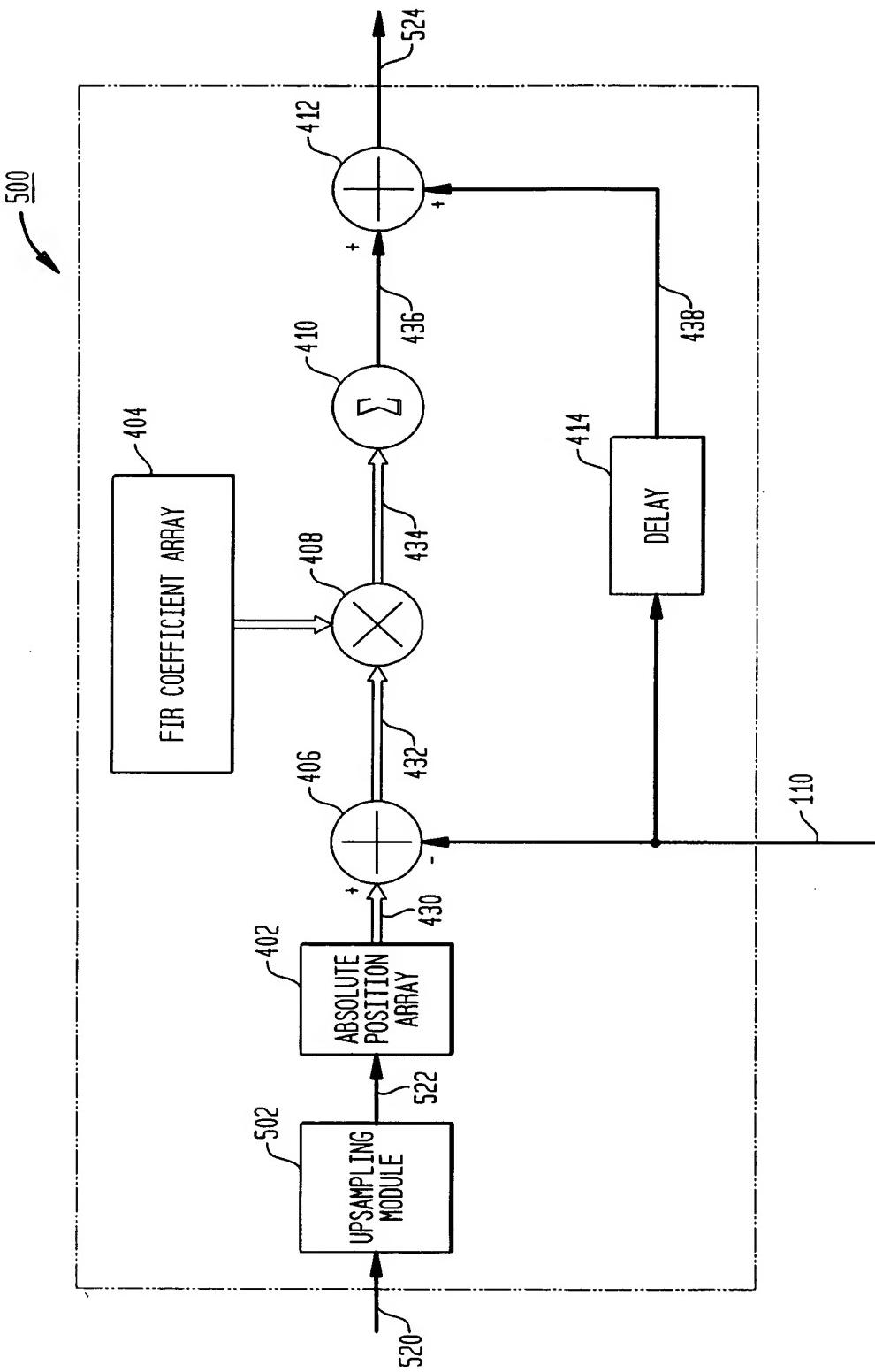
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FIG. 4



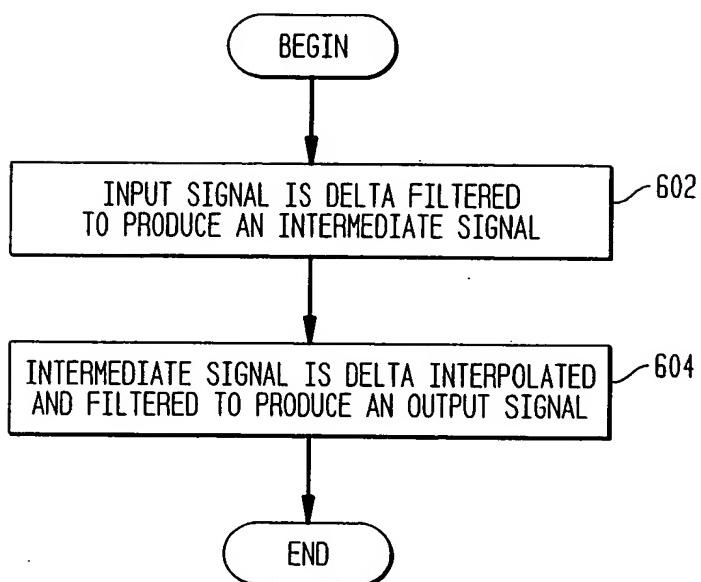
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FIG. 5



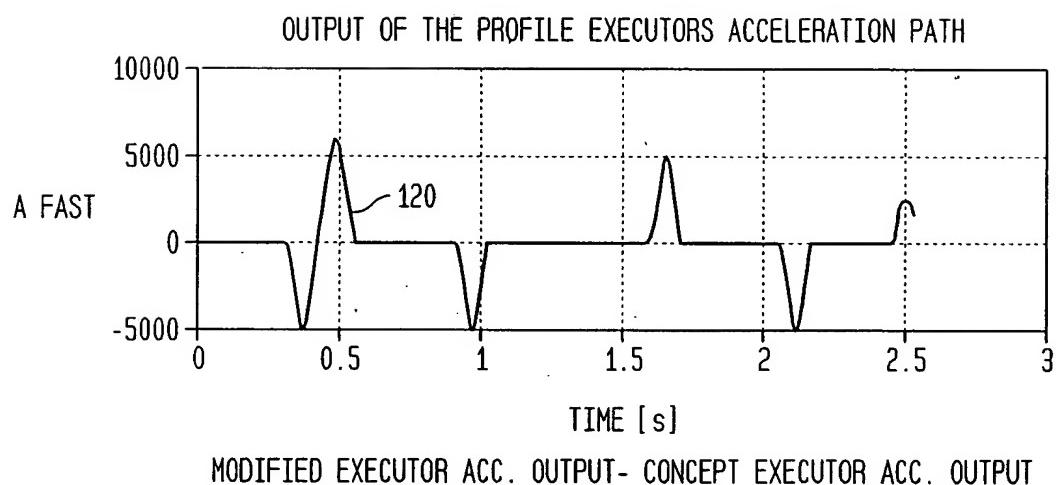
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FIG. 6



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FIG. 7



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FIG. 8

INTERPOLATION FILTERS FREQUENCY COMPARISON

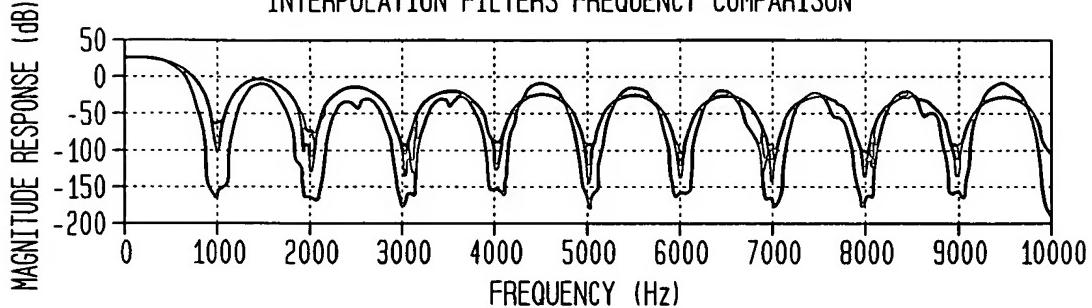


FIG. 9

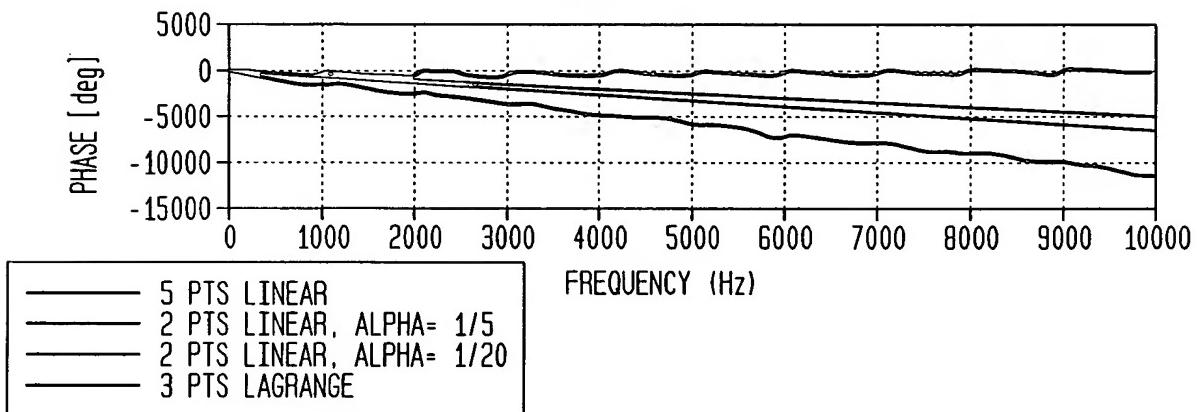
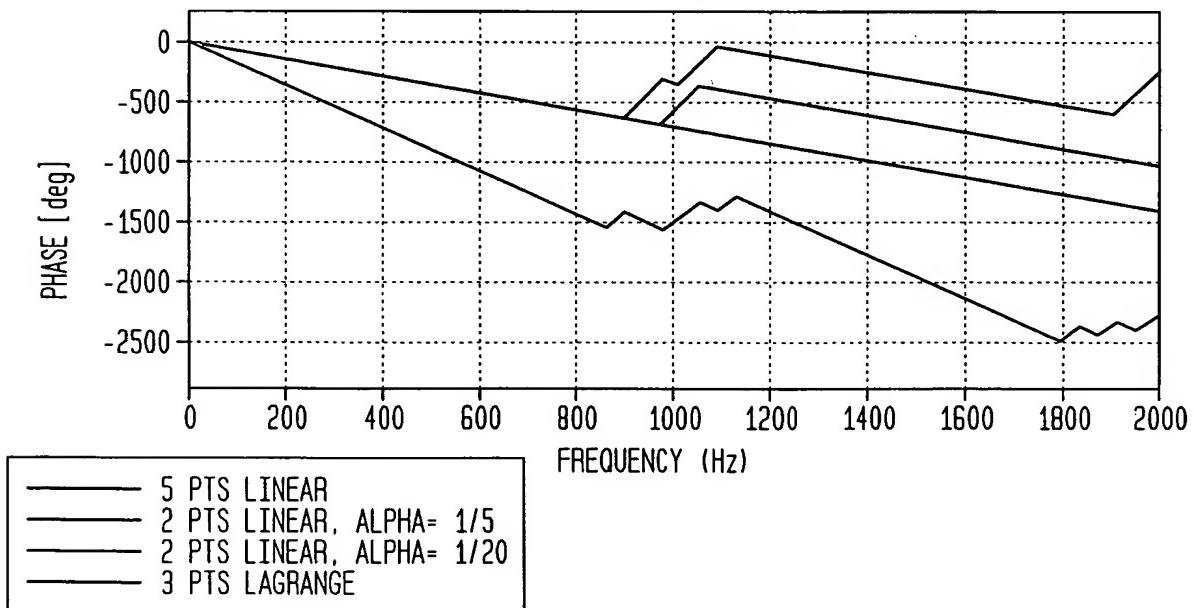
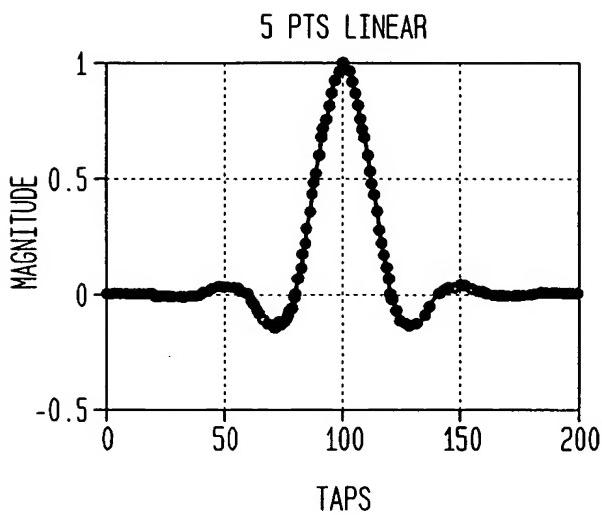
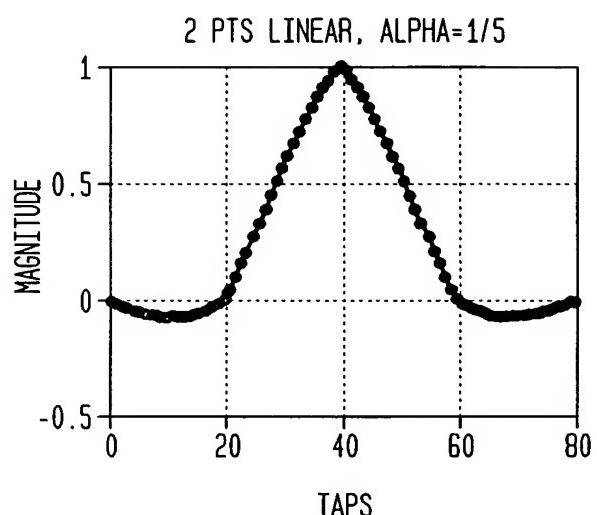
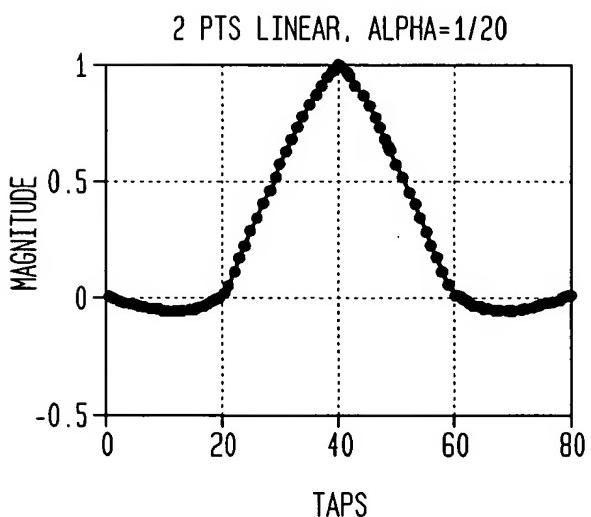
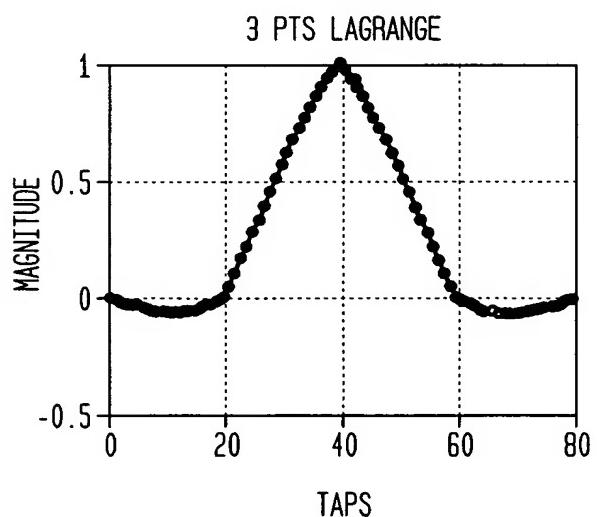


FIG. 10

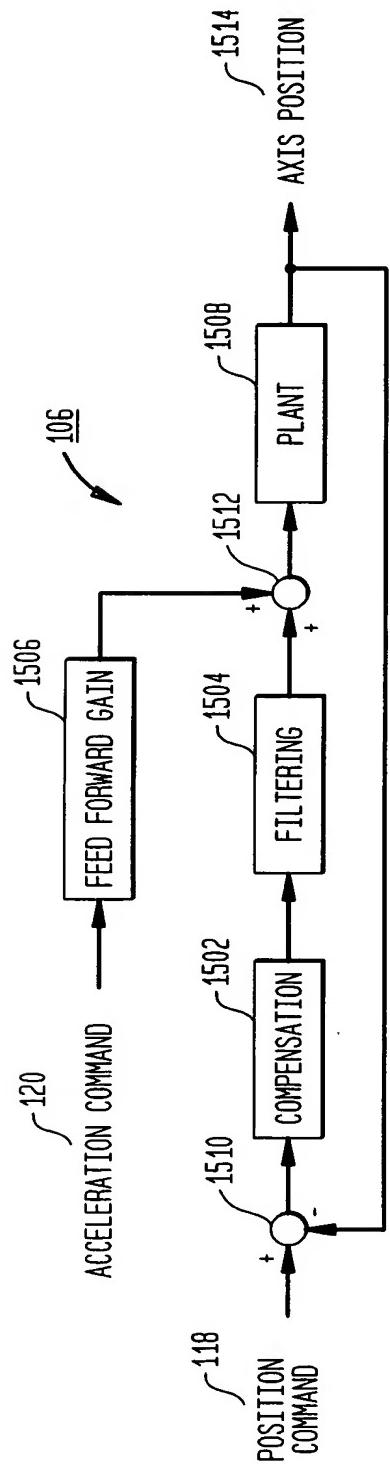


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FIG. 11*FIG. 12**FIG. 13**FIG. 14*

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FIG. 15



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FIG. 16A

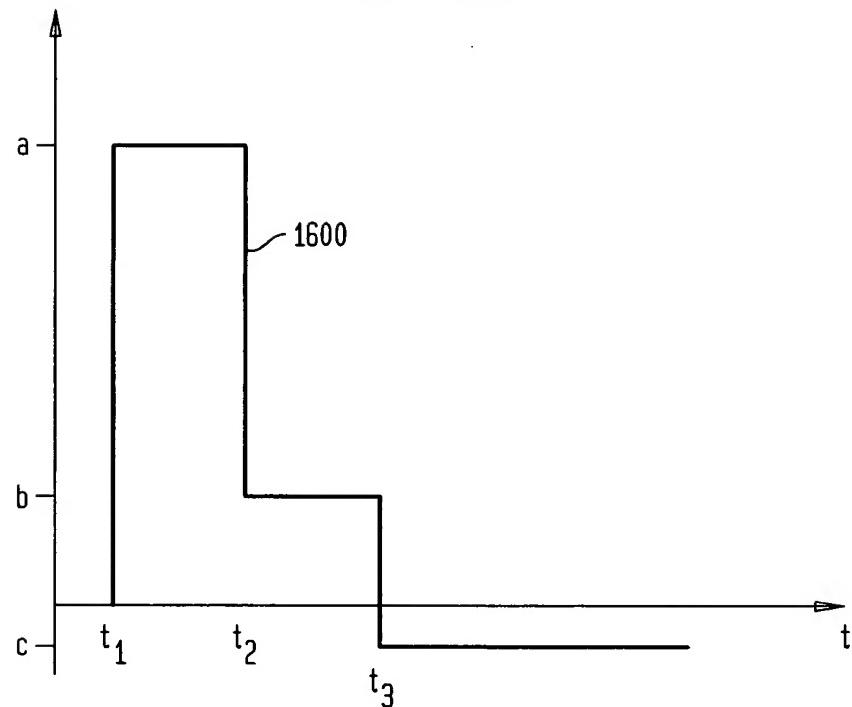
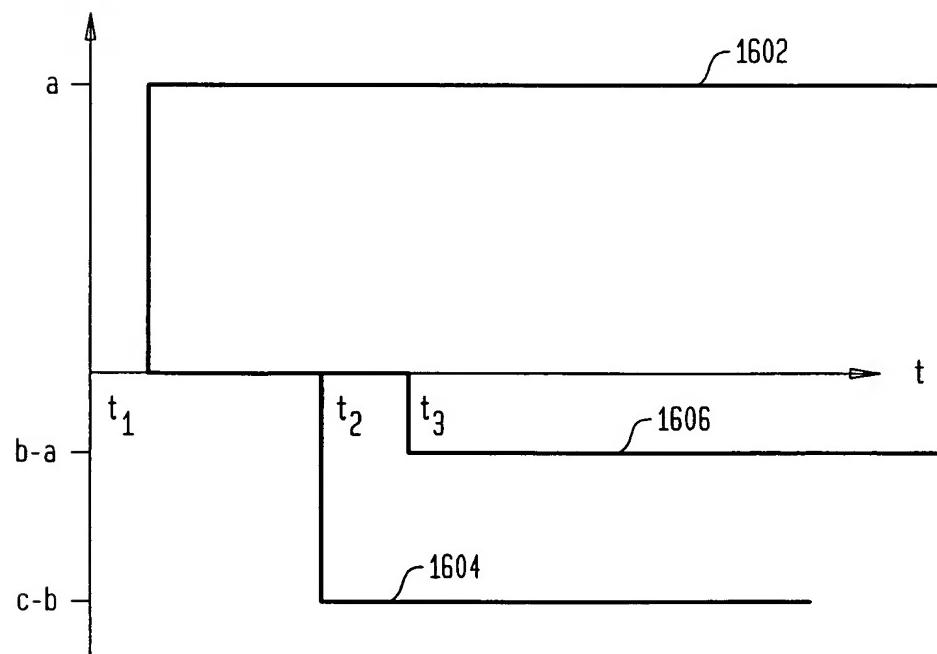
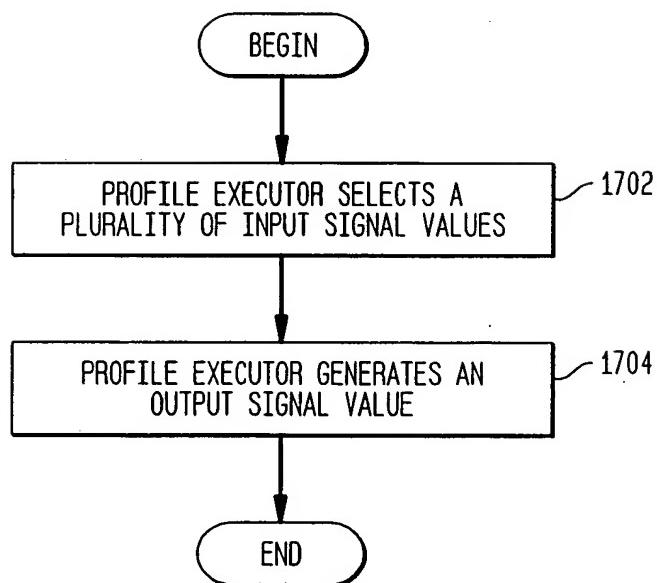


FIG. 16B



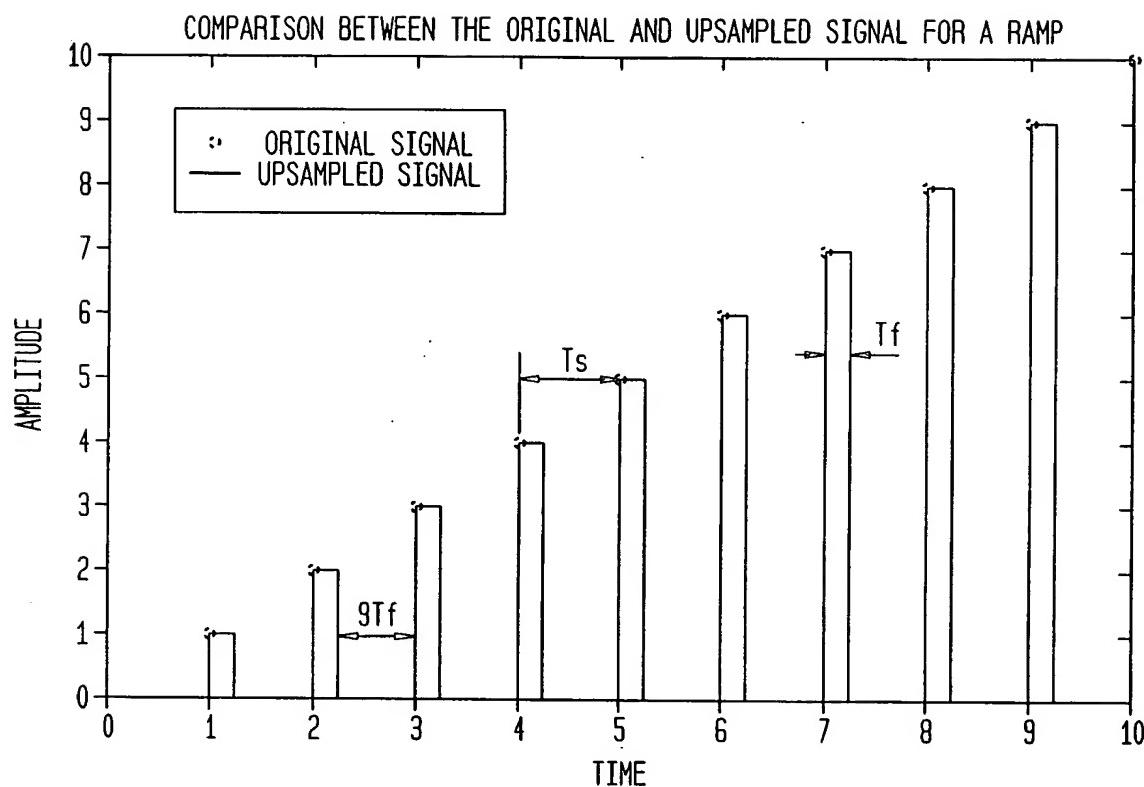
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FIG. 17



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FIG. 18



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FIG. 19

